

EMBEDDED TECHNOLOGY™ SERIES



Real-Time UML Workshop for Embedded Systems



**CD-ROM
INCLUDED
CONTAINING**

**Demo version
of the
Rhapsody
UML tool**

**Models of the
solutions**

Bruce Powel Douglass



Real-Time UML Workshop for Embedded Systems

This Page Intentionally Left Blank

**Real-Time UML Workshop
for Embedded Systems**
by Bruce Powel Douglass, Ph.D.



AMSTERDAM • BOSTON • HEIDELBERG • LONDON
NEW YORK • OXFORD • PARIS • SAN DIEGO
SAN FRANCISCO • SINGAPORE • SYDNEY • TOKYO

Newnes is an imprint of Elsevier



Newnes is an imprint of Elsevier
30 Corporate Drive, Suite 400, Burlington, MA 01803, USA
Linacre House, Jordan Hill, Oxford OX2 8DP, UK

Copyright © 2007, Elsevier Inc. All rights reserved.

No part of this publication may be reproduced, stored in a retrieval system, or transmitted in any form or by any means, electronic, mechanical, photocopying, recording, or otherwise, without the prior written permission of the publisher.

Permissions may be sought directly from Elsevier's Science & Technology Rights Department in Oxford, UK: phone: (+44) 1865 843830, fax: (+44) 1865 853333, e-mail: permissions@elsevier.com.uk. You may also complete your request on-line via the Elsevier homepage (<http://elsevier.com>), by selecting "Support & Contact," then "Copyright and Permission" and then "Obtaining Permissions."



Recognizing the importance of preserving what has been written,
Elsevier prints its books on acid-free paper whenever possible.

Library of Congress Cataloging-in-Publication Data

Application submitted

British Library Cataloguing-in-Publication Data

A catalogue record for this book is available from the British Library.

ISBN-13: 978-0-7506-7906-0

ISBN-10: 0-7506-7906-9

For information on all Newnes publications
visit our website at www.books.elsevier.com

07 08 09 10 10 9 8 7 6 5 4 3 2 1

Printed in the United States of America

Working together to grow
libraries in developing countries

www.elsevier.com | www.bookaid.org | www.sabre.org

ELSEVIER

BOOK AID
International

Sabre Foundation

*This book is dedicated to my family—
two boys who make me proud,
a wife any man would envy,
and a stepdaughter who is so bright, she's scary.
It's good to be me—thanks, guys!*

This Page Intentionally Left Blank

Contents

Preface	xiii
<i>Audience</i>	<i>xiv</i>
<i>Goals</i>	<i>xv</i>
<i>Where to Go After the Book</i>	<i>xv</i>
<i>Evaluate UML on ARM</i>	<i>xv</i>
Acknowledgments	xvii
About the Author	xix
What's on the CD-ROM?	xxi
Chapter 1: Introduction	1
<i>Basic Modeling Concepts of the UML</i>	<i>1</i>
<i>Structural Elements and Diagrams</i>	<i>5</i>
Small Things: Objects, Classes, and Interfaces	<i>5</i>
Relations	<i>10</i>
Big Things: Subsystems, Components, and Packages	<i>17</i>
<i>Behavioral Elements and Diagrams</i>	<i>19</i>
Actions and Activities	<i>19</i>
Operations and Methods	<i>20</i>
Activity Diagrams	<i>20</i>
Statecharts	<i>22</i>
Interactions	<i>27</i>
<i>Use Case and Requirements Models</i>	<i>31</i>
<i>Summary</i>	<i>33</i>
<i>Check Out the CD-ROM</i>	<i>33</i>
Chapter 2: The Harmony Process	35
<i>Introduction</i>	<i>35</i>
<i>The Harmony Development Process</i>	<i>36</i>
Why Process?	<i>36</i>
Harmony Process Overview	<i>41</i>

The Systems Engineering Harmony Workflows in Detail	43
The Incremental (Spiral) Development Workflows in Detail	47
Increment Review (Party!) Workflow	48
Design with the Harmony Process	55
Implementation	60
Test	61
<i>Summary</i>	63
Chapter 3: Specifying Requirements	65
<i>Overview</i>	65
<i>Problem 3.1 Identifying Kinds of Requirements for Roadrunner</i>	
<i>Traffic Light Control System</i>	67
<i>Problem 3.2 Identifying Use Cases for the Roadrunner</i>	
<i>Traffic Light Control System</i>	69
<i>Problem 3.3 Mapping Requirements to Use Cases</i>	69
<i>Problem 3.4 Identifying Use Cases for the Coyote UAV System</i>	70
<i>Problem 3.5 Identifying Parametric Requirements</i>	70
<i>Problem 3.6 Capturing Quality of Service Requirements in Use Cases</i> . .	71
<i>Problem 3.7 Operational View: Identifying Traffic Light Scenarios</i>	71
<i>Problem 3.8 Operational View: CUAWS Optical Surveillance</i>	
<i>Scenarios</i>	73
<i>Problem 3.9 Specification View: Use-Case Description</i>	73
<i>Specification View: State Machines for Requirements Capture</i>	74
<i>Problem 3.10 Specification View: Capturing Complex Requirements</i> . .	76
<i>Problem 3.11 Operational to Specification View: Capturing</i>	
<i>Operational Contracts</i>	77
<i>References</i>	82
Chapter 4: Systems Architecture	83
<i>Overview</i>	83
<i>Problem 4.1 Organizing the Systems Model</i>	85
<i>Problem 4.2 Subsystem Identification</i>	90
<i>Problem 4.3 Mapping Operational Contracts into</i>	
<i>Subsystem Architecture</i>	92
<i>Problem 4.4 Identifying Subsystem Use Cases</i>	101
<i>Looking Ahead</i>	107

Chapter 5: Object Analysis	109
<i>Overview</i>	109
<i>Key Strategies for Object Identification</i>	111
Underline the Noun Strategy.	113
Identify the Causal Agents.	113
Identify Services (Passive Contributors or Server Objects)	114
Identify Messages and Information Flows	114
Identify Real-World Items.	114
Identify Physical Devices.	114
Identify Key Concepts.	115
Identify Transactions.	115
Identify Persistent Information	115
Identify Visual Elements	115
Identify Control Elements.	116
Apply Scenarios.	116
<i>Problem 5.1 Apply Nouns and Causal Agents Strategies.</i>	116
<i>Problem 5.2 Apply Services and Messages Strategies.</i>	125
<i>Problem 5.3 Apply Real-World Items and Physical Devices Strategies</i>	127
<i>Problem 5.4 Apply Key Concepts and Transaction Strategies</i>	128
<i>Problem 5.5 Apply Identify Visual Elements and Scenarios Strategies</i>	128
<i>Problem 5.6 Merge Models from the Various Strategies</i>	137
<i>Looking Ahead</i>	139
Chapter 6: Architectural Design	141
<i>Overview</i>	141
<i>Problem 6.1 Concurrency and Resource Architecture</i>	147
<i>Problem 6.2 Distribution Architecture</i>	158
<i>Problem 6.3 Safety and Reliability Architecture</i>	163
<i>Looking Ahead</i>	177
Chapter 7: Mechanistic and Detailed Design	179
<i>Overview</i>	179
<i>Mechanistic Design</i>	180
Delegation Pattern Strategy.	183
Interface Abstraction Pattern Strategy	185
<i>Detailed Design.</i>	187
<i>Problem 7.1 Applying Mechanistic Design Patterns—Part 1.</i>	192

<i>Problem 7.2 Applying Mechanistic Design Patterns—Part 2</i>	<i>196</i>
<i>Problem 7.3 Applying Detailed-Design State Behavior Patterns</i>	<i>201</i>
<i>Problem 7.4 Applying Detailed Design Idioms</i>	<i>206</i>
<i>Summary</i>	<i>214</i>

Chapter 8: Specifying Requirements: Answers 215

<i>Answer 3.1 Identifying Kinds of Requirements</i>	<i>215</i>
<i>Answer 3.2 Identifying Use Cases for Roadrunner Traffic</i>	
<i> Light Control System</i>	<i>216</i>
<i>Answer 3.3 Mapping Requirements to Use Cases.</i>	<i>219</i>
<i>Answer 3.4 Identifying Use Cases for Coyote UAV System</i>	<i>220</i>
<i>Answer 3.5 Identifying Parametric Requirements</i>	<i>222</i>
<i>Answer 3.6 Capturing Quality of Service Requirements.</i>	<i>223</i>
<i>Answer 3.7 Operational View: Identifying Traffic Light Scenarios</i>	<i>224</i>
<i>Answer 3.8 Operational View: CUAWS Optical Surveillance</i>	
<i> Scenarios.</i>	<i>228</i>
<i>Answer 3.9 Specification View: Use-Case Descriptions.</i>	<i>231</i>
<i>Answer 3.10 Specification View: Capturing Complex Requirements.</i>	<i>232</i>
<i>Answer 3.11 Operational to Specification View: Capturing</i>	
<i> Operational Contracts</i>	<i>238</i>
<i>References</i>	<i>242</i>

Chapter 9: Systems Architecture: Answers 243

<i>Answer 4.1 Organizing the Systems Model.</i>	<i>243</i>
<i>Answer 4.2 Subsystem Identification</i>	<i>250</i>
<i>Answer 4.3 Mapping Operational Contracts into the Subsystem</i>	
<i> Architecture.</i>	<i>256</i>
<i>Answer 4.4 Identifying Subsystem Use Cases.</i>	<i>267</i>

Chapter 10: Object Analysis: Answers 273

<i>Answer 5.1 Apply Nouns and Causal Agents Strategies</i>	<i>273</i>
<i>Answer 5.2 Apply Services and Messages Strategies</i>	<i>291</i>
<i>Answer 5.3 Applying the Real-World Items and Physical Devices</i>	
<i> Strategies.</i>	<i>297</i>
<i>Answer 5.4 Apply Key Concepts and Transaction Strategies</i>	<i>299</i>
<i>Answer 5.5 Identify Visual Elements and Scenarios Strategies</i>	<i>303</i>
<i>Answer 5.6 Merge Models from the Various Strategies</i>	<i>315</i>

Chapter 11: Architectural Design: Answers	317
<i>Answer 6.1 Concurrency and Resource Architecture.</i>	<i>317</i>
<i>Answer 6.2 Distribution Architecture.</i>	<i>319</i>
<i>Answer 6.3 Safety and Reliability Architecture</i>	<i>323</i>
Chapter 12: Mechanistic and Detailed Design: Answers . . .	339
<i>Answer 7.1 Applying Mechanistic Design Patterns—Part 1</i>	<i>339</i>
<i>Answer 7.2 Applying Mechanistic Design Patterns—Part 2</i>	<i>341</i>
<i>Answer 7.3 Applying Detailed-Design State Behavior Patterns.</i>	<i>346</i>
<i>Answer 7.4 Applying Detailed-Design Idioms.</i>	<i>351</i>
Appendix A: The Roadrunner Intersection	
Controller System Specification	357
<i>Overview</i>	<i>357</i>
<i>The Intersection Controller (IC).</i>	<i>357</i>
Configuration Parameters	358
Intersection Modes	361
The Vehicle Detector	365
Vehicular Traffic Light.	366
Pedestrian Light and Sensor	367
Front Panel Display.	368
Remote Communications	369
Power	370
Appendix B: The Coyote Unmanned Air Vehicle	
System (CUAVS) Specification	371
<i>Overview</i>	<i>371</i>
<i>Primary CUAV System Components</i>	<i>371</i>
The Unmanned Air Vehicle (UAV).	371
The Coyote Mission Planning and Control System (CMPCS)	372
Coyote Payloads	372
The Coyote Datalink Subsystem (CDS)	373
<i>Detailed Requirements</i>	<i>373</i>
The Unmanned Air Vehicle (UAV).	373
Flight Modes.	373
Mission Modes	374
<i>The Coyote Mission Planning and Control System (CMPCS).</i>	<i>374</i>

The Coyote Reconnaissance Sensor Suite Payload (CSSP) 375
The Coyote Hellfire Attack Payload (CHAP) 376
The Coyote Datalink Subsystem (CDS) 377
Appendix C: UML Notational Summary 379
Index 401

Preface

Most books on UML or real-time systems, even those that I have written, are what I call “lecture books.” They might have exercises at the end of the chapters, but they are basically organized around “Here, let me tell you about that...” This book is fundamentally different. It is meant to be a workbook, a set of guided exercises that teach by practice and example, not by exposition. As such, we won’t discuss in detail various aspects of the UML, or MDA, or even process. We will mention such things in passing, but the expectation is that the reader already has a reasonable understanding of the UML and wants practice and experience in the application of the UML to embedded and real-time systems development. This book is meant as a companion to another of my books, *Real-Time UML, Third Edition: Advances in the UML for Real-Time Systems*.¹ That book *is* a lecture book and explains, in some detail, the structural, behavioral, and functional aspects of the UML, particularly how it applies to the development of real-time and embedded systems.

The present book is organized primarily around two example problems, whose detailed problem statements can be found in the appendices. To give some breadth of experience in the exercises, both a small-scale and large-scale application are provided. The small-scale example is an intersection traffic-light control system. This is a small-scale system but is still complex enough to be a rich source of work for the reader.² The larger-scale example is an Unmanned Air Vehicle (UAV), although it is more properly called an Unmanned Combat Air Vehicle (UCAV). This system is highly complex and offers good experience in doing requirements analysis and architectural design for large-scale systems, something the traffic-light control system

¹ Douglass, Bruce Powel, *Real-Time UML, Third Edition: Advances in the UML for Real-Time Systems*, Addison-Wesley, 2004.

² One of the Laws of Douglass is “Anything is simple if you don’t have to do it!” This certainly applies in spades to the development of real-time systems, since even “simple” systems can be very complex.

cannot. The point of these examples is to provide a context for the reader to explore the application of UML to the specification, analysis, and design of such systems. It is most assuredly *not* to provide complete designs of such systems. Such systems are created by teams (or, as in the case of the UCAV, teams of teams). If the purpose of the book was to provide complete designs of such systems, the book would be between 20 and 100 times its present size.

This book can be thought of as being composed of several parts. First is a brief overview of the basics—UML and process—provided in Chapters 1 and 2. The next section is a series of chapters that walk through the phases of an incremental spiral process—requirements analysis, object analysis, architectural design, mechanistic design, and detailed design—posing problems that occur in the context of the examples. The next section provides solutions for the problems posed in the previous section. Lastly, there is some end-matter—the appendices that provide the problem statements and a brief notational guide to the UML.

All the UML diagrams in this book are created in the Rhapsody™ tool from Telelogic, and a demo copy of Rhapsody is provided in this book. The full version offers validation, model execution, and production-quality code generation facilities, as well as interfaces to other tools, such as requirements management, XMI import/export, etc. While these features are enormously helpful in actual development, they are not relevant to the purpose of the book. If you prefer to use another tool for the exercises, or even (gasp) pen and paper, you can do that as well.

The solutions are, of course, the “pointy end” of the stick. They are meant, not so much as the absolute truth or measure of goodness in UML modeling, but instead as examples of solutions to the problem. If your solution differs from the one provided and still meets the solution criteria, then that’s perfectly fine. There are always many good solutions to any problem.³ If your solution differs from the provided solution, I recommend that you study the differences between the two and determine under which conditions one or the other would be preferable.

Audience

The book is oriented towards the practicing professional software developer and the computer science major, in the junior or senior year. It focuses on practical experience by solving posed problems from the context of the examples. The book assumes a reasonable proficiency in UML and at least one programming language (C++ is used

³ There are always even more *bad* solutions to any problem as well!

in this book, but C, Java, Ada, or any equivalent 3GL can be used). This book is meant as a companion to the author's *Real-Time UML, Third Edition: Advances in the UML for Real-Time Systems*, but that book is not required to use this workbook. Other books that may help the reader by providing more depth in various topic areas include the author's *Doing Hard Time: Developing Systems with UML, Objects, Frameworks and Patterns*, Addison-Wesley, 1999, and *Real-Time Design Patterns: Robust Scalable Architecture for Real-Time Systems*, Addison-Wesley, 2002.

Goals

The goal of this book is simply to give the reader practical experience in modeling different aspects of real-time and embedded systems. While there is some introduction early in the first two chapters of the book and at the beginning of the problem chapters, the goal is to pose problems for the reader to model. The book includes a demo version of the powerful Rhapsody™ tool, but the use of that tool is not required to go through the exercises.

By the time you've reached the end of the book, you will hopefully have the expertise to tackle the real modeling issues that you face in your life as a professional developer of real-time and embedded systems.

Where to Go After the Book

If you're interested in tools, training, or consulting, see www.telelogic.com or www.ilogix.com. The author teaches classes and consults worldwide on UML, MDA, DoDAF, architectural design, design patterns, requirements modeling, use cases, safety critical development with UML, behavioral modeling, the development process improvement, project management and scheduling, and quite a bit more. You can contact him for training or consulting services at Bruce.Douglass@telelogic.com. He also runs a (free) yahoo group forum at <http://groups.yahoo.com/group/RT-UML>—come on down! The I-Logix website also has many white papers available for downloading on different topics that may be of interest.

Evaluate UML on ARM

On the CD-ROM with this book, you will find the Rhapsody UML tool from Telelogic (for further information, see *What's on the CD-ROM?*). Do you want to evaluate the UML on an ARM microcontroller using C? Willert Software Tools delivers an evaluation version for the UML for generating ARM software. You can download this at http://www.willert.de/rxf_eval.

The evaluation software contains the following:

- Telelogic Rhapsody demo version
- Keil MicroVision/ARM compiler
A good ARM compiler with a very usable IDE. Compiler, simulator and debugger integrated in a nice package.
- Willert WST52lt Bridge
The Willert Framework RXF (modified version of the Rhapsody framework) optimized for small microcontrollers, code size under 4k ROM and 200 bytes of RAM, where speed and code size are critical.

The evaluation versions are limited but fully functional for the evaluation models. The generated models run in the Keil simulator. If you want to see the models run on real hardware you can order an evaluation kit for an attractive price. This kit contains a Keil MCB2130 Board with the Philips ARM7 chip and a uLink USB/JTAG debugger interface.

Bruce Powel Douglass, Ph.D.
Summer, 2006

Acknowledgments

I want to thank my editor, Tiffany Gasbarrini, for nagging me when I clearly needed it and being supportive when I need that too. My reviewers—Ian Macafee, Markus Rauber, Mark Richardson, Bart Jenkins, Frank Braun, and David McKean—were also helpful in keeping me honest. I have no doubt that errors remain, and I claim full responsibility for those. Most of all, I want to thank my family for keeping me sane, or at least trying to, while I burned the midnight electrons, creating this book.

This Page Intentionally Left Blank

About the Author

Bruce was raised by wolves in the Oregon wilderness. He taught himself to read at age 3 and calculus before age 12. He dropped out of school when he was 14 and traveled around the U.S. for a few years before entering the University of Oregon as a mathematics major. He eventually received his M.S. in exercise physiology from the University of Oregon and his Ph.D. in neurophysiology from the University of South Dakota Medical School, where he developed a branch of mathematics called autocorrelative factor analysis for studying information processing in multicellular biological neural systems.

Bruce has worked as a software developer in real-time systems for over 25 years and is a well-known speaker, author, and consultant in the area of real-time embedded systems. He is on the Advisory Board of the *Embedded Systems* and *UML World* conferences, where he has taught courses in software estimation and scheduling, project management, object-oriented analysis and design, communications protocols, finite state machines, design patterns, and safety-critical systems design. He develops and teaches courses and consults in real-time object-oriented analysis and design and project management and has done so for many years. He has authored articles for many journals and periodicals, especially in the real-time domain.

He is the Chief Evangelist¹ for Telelogic (formerly I-Logix), a leading producer of tools for real-time systems development. Bruce worked with Rational and the other UML partners on the specification of the UML. He is one of the co-chairs of the Object Management Group's Real-Time Analysis and Design Working Group. He is the author of several other books on software, including *Doing Hard Time: Developing Real-Time Systems with UML, Objects, Frameworks and Patterns* (Addison-Wesley, 1999), *Real-Time Design Patterns: Robust Scalable Architecture for Real-Time*

¹ Being a Chief Evangelist is much like being a Chief Scientist, except for the burning bushes and stone tablets.

Systems (Addison-Wesley, 2002), *Real-Time UML, Third Edition: Advances in the UML for Real-Time Systems* (Addison-Wesley, 2004), and several others, including a short textbook on table tennis.

Bruce enjoys classical music and has played classical guitar professionally. He has competed in several sports, including table tennis, bicycle racing, running, and full-contact Tae Kwon Do, although he currently only fights inanimate objects that don't hit back.

Bruce does extensive consulting and training throughout the world. If you're interested, contact him at *Bruce.Douglass@telelogic.com*.

What's on the CD-ROM?

- Demo version of the Rhapsody UML tool from Telelogic and various models of the solutions.
- README.TXT file in the root directory of the CD-ROM contains detailed instructions for how to install the software and obtain a temporary license to use the tool while working through the exercises in the book.